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KINTEX_7_FPGA_EMBEDDED_TARGETED_REFERENCE_DESIGN

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The KC705 Embedded Kit MicroBlaze™ Proces sor Subsystem showcases various features of the KC705 evaluation board. The targeted reference designs (TRDs) are built around a MicroBlaze soft processor with various peripherals to enable embedded applications.

[AR# 56174: Kintex-7 FPGA Embedded Targeted Reference ...](#)

Kintex-7 FPGA Embedded Kit Equipped with a combination of silicon, tools, IP cores, and MicroBlaze soft processor-based reference designs, both software and hardware developers alike can work on their designs in parallel. The kit has all the components needed for designers to get started, including: KC705 baseboard with Kintex-7 325T FPGA

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Xilinx GmbH Germany, Xilinx, Inc. (NASDAQ: XLNX) today announced the Kintex(TM) -7 FPGA Embedded Kit, providing a ready-to-use development pl...

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Kintex-7 FPGA KC705 Embedded Kit Getting Started Guide Vivado Design Suite 2013.2 UG913 (v4.1.1) March 7, 2014 0402910-03

[Kintex-7 FPGA Embedded Kit FAQ](#)

UG985 - Kintex-7 FPGA Embedded Targeted Reference Design User Guide (Vivado Design Suite 2013.2) (ver1.0, 970 KB) [PDF] This document describes the KC705 Embedded Kit MicroBlaze™ Processor Subsystem. The targeted reference designs (TRDs) are built around the MicroBlaze soft processor.

[Kintex-7 FPGA Embedded Kit - Xilinx | Mouser India](#)

The Getting Started with the Kintex-7 FPGA KC705 Embedded Kit User Guide (UG913) v1.0 does not reference the J69 jumper. How should this jumper be set to ensure the Targeted Reference Design runs correctly?

[EDN - Xilinx Kintex-7 FPGA Embedded Kit Accelerates ...](#)

Xilinx Kintex®-7 FPGA Embedded Kit includes the components of the KC705 Base Evaluation Kit plus all additional soft content that embedded designers need to quickly design their high-performance embedded systems.

[Xilinx Wiki - K7 Embedded TRD 2013.2](#)

The Kintex®-7 FPGA Connectivity Kit is a 20Gb/s platform for high-bandwidth and high-performance applications containing all the necessary hardware, software and IP to power quickly through your evaluation and development of connectivity systems.

[Xilinx Kintex-7 FPGA Embedded Kit Accelerates Productivity ...](#)

"Designers can reduce development time by using the Kintex-7 FPGA embedded targeted reference design that comes with the kit as a stepping stone for building their customized embedded systems," said Raj Seelam, Xilinx Senior Marketing Manager of Targeted Design Platforms.

[Kintex-7 FPGA Embedded Kit](#)

/PRNewswire/ -- Xilinx, Inc. (NASDAQ: XLNX) today announced the Kintex™-7 FPGA Embedded Kit, providing a ready-to-use development platform for system designers...

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Xilinx Kintex-7 FPGA Embedded Kit Accelerates Productivity and Programmable System Integration for FPGA-Based Soft Processor Systems: Xilinx, Inc. (NASDAQ: XLNX) today announced the Kintex™-7 FPGA Embedded Kit, providing a ready-to-use development platform for system designers to quickly and easily implement programmable system integration for applications where the processor needs to ...

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The AXI Interface Based KC705 Embedded Kit MicroBlaze Processor Subsystem Software Tutorial (UG915) explains the procedure for Profiling Stand-Alone Software Applications on BIST hardware. Are there any known issues with this flow using the 14.4 design tools?

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Xilinx Inc. releases Vivado Design Suite 2012.3 design environment that offers designers running the tools on multi-core processor workstations productivity enhancements and new reference designs.. The Vivado Design Suite is said to cut implementation time from C and RTL by up to four times for complex designs, and to increase performance with up to one speed grade advantage over the ISE ...

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SDSoC Development Environment from XILINX, Inc.. The SDSoCâ„¢ development environment provides a greatly simplified ASSP-like C/C++ programming experience including an easy to use Eclipse IDE and a comprehensive design environment for heterogeneous Zynq® All Programmable SoC and MPSoC deployment.

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